HIGH-SPEED COMMUNICATION SYSTEM WITH A FEEDBACK SYNCHRONIZATION LOOP

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ABSTRACT OF THE DISCLOSURE

In a communications device having a physical layer device and a processing device connected to the physical layer device, the number of input/output (I/O) ports required for communication between the devices in the gigabit range is substantially reduced by utilizing millivolt differential I/O drivers and receivers. In addition, a calibration feedback loop synchronizes the data and clock signals on the processing device, thereby eliminating the need to recover the clock on the processing device.